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**METHOD AND SYSTEM FOR IMPROVING DYNAMIC RANGE FOR
COMMUNICATION SYSTEMS USING UPSTREAM ANALOG INFORMATION**

**CROSS-REFERENCE TO RELATED APPLICATIONS/INCORPORATION BY
REFERENCE**

[01] This application makes reference to, claims priority to, and claims the benefit of United States Provisional Application Serial No. _____ (Attorney Docket No. 15888US01) filed March 8, 2004.

[02] The above stated application is hereby incorporated herein by reference in its entirety.

FIELD OF THE INVENTION

[03] Certain embodiments of the invention relate to packet based wireless communication systems. More specifically, certain embodiments of the invention relate to a method and system for improving dynamic range for communication systems such as 802.11 systems using upstream analog information.

BACKGROUND OF THE INVENTION

[04] In packet-based wireless communication systems, a transmitted packet may be received with a large range of signal strengths, that is, a wide dynamic range. For example, in an 802.11 system, there may be as much as a 100 dB difference in signal strength between packets received at receiver A sent from transmitter B versus a packet received by receiver A sent from transmitter C. Factors accounting for this variation include path loss and fading characteristics of the channel, for example. Path loss may include attenuation losses incurred due to the distance existing between a

transmitter and a receiver. Fading characteristics of the channel may include multipath interference destructively combining to reduce the strength of the signal received at the receiver. A well-designed communication transceiver must perform reliably given these impairments that are characteristic of wireless media. In this regard, a goal of a well-designed communication transceiver is to mitigate these characteristic impairments. In order to achieve this goal, a practical receiver may make use of automatic gain control (AGC). Automatic gain control can be described as an algorithm that may be adapted to automatically adjust signal size in order to maximize some parameter.

[05] FIG. 1 is a block diagram of a conventional receiver system that utilizes gain control. Referring to FIG. 1, the conventional receiver comprises a mixer 102, a gain block 104, analog-to-digital converter (ADC) 106 and gain control block 108. The conventional receiver may be part of a packet-based wireless system, which is adapted to receive a signal that is transmitted at a particular carrier frequency.

[06] In operation, the mixer 102 receives an input received signal and mixes the received signal with a tuning frequency to generate a baseband signal. The gain block 104 applies an initial gain G_{initial} to the baseband signal, and the AGC algorithm will apply a final gain output gain G_{final} to the data portion of the packet. The analog to digital converter (ADC) 106 converts the analog signal to digital samples, which are subsequently processed.

[07] A good AGC algorithm that may be implemented in the gain block 108, is adapted to choose or provide a final gain value G_{final} dB to apply to the data portion of the packet such that the signal to quantization noise ratio out of the ADC is maximized. Additionally, the final gain value G_{final} dB is chosen so that it is not too large as to cause an overflow to occur at the ADC during reception of the packet. The first criterion maximizes the signal to quantization noise ratio (SQNR) for the packet, and the second criterion prevents the packet from almost certainly being received with errors due to signal distortion. A well-designed gain block 108 is configured to execute an AGC algorithm that will accomplish this task.

[08] Referring to FIG. 1, in L_1 and L_2 represents the limits of the ADC 106. In case 1, G_{final} is too small and the resulting analog signal, which is an input to the ADC 106, does not optimally utilize the limits L_1 and L_2 of the ADC 106. Accordingly, the AGC algorithm would have made a poor decision or choice. In case 2, G_{final} is too large and the resulting analog signal, which is an input to the ADC 106, does not optimally utilize the limits L_1 and L_2 since these limits of the ADC 106 are exceeded. Since the limits L_1 , L_2 of the ADC are exceeded, clipping of the signal occurs. Accordingly, the AGC algorithm would have made a poor decision or choice. In case 3, G_{final} is ideal and the resulting analog signal, which is an input to the ADC 106, optimally utilizes the limits L_1 and L_2 of the ADC 106. In this case, no clipping of the analog signal occurs. Accordingly, the AGC algorithm would have made an ideal decision or choice.

[09] For 802.11 orthogonal frequency division multiplexing (OFDM) systems, the gain G_{final} is calculated and applied during the preamble portion of the packet. The preamble of the packet is relatively short in time compared to the overall packet length, and corrections for other system impairments such as frequency offset may also need to be calculated during this portion of the transmission. Thus, the amount of time needed to determine the proper gain setting for the received packet needs to be kept small. For a practical 802.11a/g orthogonal frequency division multiplexing system, this means it is likely at most one intermediate gain setting $G_{intermediate}$ is allowed during the preamble to determine the final gain G_{final} .

[10] FIG. 2 is a diagram illustrating the application of gain to a packet. Referring to FIG. 2, there is shown a packet 200 having a preamble portion 202 and a data portion 204. The leftmost portion of the packet 200 is the demarcation of the start of packet (SOP) and the rightmost portion of the packet 200 is the demarcation of the end-of-packet EOP. The gain G_{final} is applied at reference A, which occurs during the preamble portion 202 of the packet 200. In this case, G_{final} is greater than $G_{initial}$ ($G_{final} > G_{initial}$).

[11] FIG. 3 is a diagram illustrating the application of gain to a packet. Referring to FIG. 3, there is shown a packet 300 having a preamble portion 302 and a data portion 304. The leftmost portion of the packet 300 is the demarcation of the start of packet

(SOP) and the rightmost portion of the packet 300 is the demarcation of the end-of-packet EOP. A gain G_{initial} is in effect at the start-of-packet (SOP) where clipping is occurring. A gain $G_{\text{intermediate}}$ is applied at reference B where no clipping occurs but the signal is too small. A gain G_{final} is applied at reference C where no clipping occurs and the signal is ideal. In this case, G_{initial} , $G_{\text{intermediate}}$ and G_{final} are applied during the preamble.

[12] In order for a receiver to detect small receiver signal input, the initial front-end gain G_{initial} must necessarily be set to a large value. However, if the incoming signal is in fact large, the signal level at the output of the ADC will be clipped, making it difficult to determine the received signal power. That is, if a received signal power of X dBm is enough to cause a clip at the ADC, then all received signal powers greater than X dBm also cause a clip.

[13] Further limitations and disadvantages of conventional and traditional approaches will become apparent to one of skill in the art, through comparison of such systems with some aspects of the present invention as set forth in the remainder of the present application with reference to the drawings.

BRIEF SUMMARY OF THE INVENTION

[14] Certain embodiments of the invention may be found in a method and system for improving dynamic range using upstream analog information. Aspects of the method may comprise generating a plurality of upstream analog signals for a received signal. Upstream analog information related to at least a portion of the generated plurality of upstream analog signals may be acquired. A gain for the received signal may be adjusted using at least a portion of the acquired upstream analog information to increase dynamic range of the received signal.

[15] The received signal is low pass filtered to generate a plurality of narrowband analog signals. At least one sample may be acquired from at least a portion of the generated plurality of upstream analog signals and a power for the received signal may be computed based on the acquired sample or samples. A determination may be made as to whether the generated plurality of upstream analog signals is clipped. An intermediate gain may be generated based on the computed power of the acquired sample and applied to one or more of the generated plurality of upstream analog signals if the signal is clipped. The computed power may be compared to a plurality of defined power values and a gain selected based on the comparison. The defined power values may be stored in lookup table, for example. A final gain may be applied to the received signal. The generated plurality of upstream analog signals may be converted to corresponding time domain signals.

[16] Another embodiment of the invention may provide a machine-readable storage, having stored thereon, a computer program having at least one code section executable by a machine, thereby causing the machine to perform the steps as described above for processing received signals in a communication system.

[17] Another embodiment of the invention provides a system for processing received signals. Aspects of the system may comprise a receiver that generates a plurality of upstream analog signals for a received signal. The generated plurality of upstream analog signals may be narrowband analog signals. At least one processor may acquire

upstream analog information related to at least a portion of the generated plurality of upstream analog signals. At least one automatic gain controller may be adapted to adjust a gain for the received signal using at least a portion of the acquired upstream analog information to increase a dynamic range of the received signal.

[18] The system may further comprise at least one low pass filter that filters the received signal. The processor may acquire at least one sample from at least a portion the generated plurality of upstream analog signals and compute a power based on the acquired sample. The processor may be adapted to determine when at least one of the generated plurality of upstream analog signal is clipped. The automatic gain controller may generate an intermediate gain based on the computed power of the acquired sample. The processor may apply the generated intermediate gain to the generated plurality of upstream analog signals. After comparing the computed power to a plurality of defined power values, which may be stored in a lookup table, the processor may select a gain based on a comparable power value. The automatic gain controller may be utilized to apply a final gain to the received packet. The receiver may be adapted to convert the generated plurality of upstream analog signals to corresponding time domain signals.

[19] Another embodiment of the invention provides a receiver for processing received communication signals. The receiver may comprise a mixer, a low pass filter coupled to the mixer and a plurality of gain block serially coupled to an output of the low pass filter. The system may also comprise a plurality of analog to digital converters, wherein an input of a first of the analog-to-digital converters is coupled to the output of the low pass filter. An input of each of a remaining portion of the analog-to-digital converters is individually coupled to a corresponding output of each of the serially coupled gain blocks. An output path traced from the output of the low pass filter to an output of each of the analog-to-digital converters may be referred to as a processing path. Accordingly, each of the processing paths may comprise a gain controller and an analog to digital converter, except for the first processing path, which may have an ADC coupled directly to the output of the low pass filter.

[20] These and other advantages, aspects and novel features of the present invention, as well as details of an illustrated embodiment thereof, will be more fully understood from the following description and drawings.

BRIEF DESCRIPTION OF SEVERAL VIEWS OF THE DRAWINGS

[21] FIG. 1 is a block diagram of a conventional receiver system that utilizes gain control.

[22] FIG. 2 is a diagram illustrating the application of gain to a packet.

[23] FIG. 3 is a diagram illustrating the application of gain to a packet.

[24] FIG. 4 is a block diagram of a receiver that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention.

[25] FIG. 5 is a block diagram illustrating variations in signal sizes as gain is applied to a baseband signal in the processing chain of FIG. 4 in accordance with an embodiment of the invention.

[26] FIG. 6 is a block diagram of a receiver that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention.

[27] FIG. 7 is a flow chart illustrating exemplary steps that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention.

[28] FIG. 8a is a diagram illustrating a case having no WRSSI and/or NBDO-x information, which may be utilized in connection with improving dynamic range using upstream analog information in accordance with an embodiment of the invention.

[29] FIG. 8b is a diagram illustrating a case having WRSSI and/or NBDO-x information, which may be utilized in connection with improving dynamic range using upstream analog information in accordance with an embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

[30] Certain aspects of the invention may be found in a method and system for improving dynamic range using upstream analog information. One aspect of the invention employs narrow band direct out (NBDO) signal and/or wideband received signal strength indicator (WRSSI) aided automatic gain control (NBDO/WRSSI-aided AGC) which observes the received input signal further upstream in the processing chain such as in the earlier stages of analog processing. Digital samples of NBDO information may be utilized to determine a more accurate gain to be applied in order to provide a more accurate calculation of the received signal power for automatic gain control. Accordingly, a wide range in the received input powers may be more efficiently and accurately demodulated.

[31] In one aspect of the invention, a correct gain setting which is to be applied may be determined by observing a received signal before it reaches an analog to digital converter (ADC). In other words, upstream analog information may be utilized to determine an optimal gain that should be applied to the received signal. Digital samples of narrowband direct out (NBDO) information generated by low pass filtering the received signal may be utilized to provide a more accurate calculation of the received signal power for automatic gain control. Accordingly, after applying the determined gain, a wider range in the received input powers may be correctly demodulated.

[32] FIG. 4 is a block diagram of a receiver that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention. Referring to FIG. 4, the receiver comprises a mixer 402, a low pass filter (LPF) block 404, a plurality of gain (G) control blocks (GCs) 406a, 406b, ..., 406n, and analog-to-digital converter (ADC) block 408. The receiver may be part of a packet-based wireless system, which may be adapted to receive a signal that is transmitted at a particular carrier frequency.

[33] In operation, the mixer 402 receives an input received signal X and mixes the received signal with a tuning frequency to generate a baseband signal. The resulting

baseband signal may be referred to as a wideband received signal (WRS) and an indication of its signal strength may be referred to as a wideband received signal strength indicator (WRSSI). The resulting baseband signal is low pass filtered by the low pass filter block 404 to generate a narrowband signal which may be referred to as a first narrowband direct output (NBDO-1) signal.

[34] In the analog processing chain of FIG. 4, an overall gain G may be applied in a plurality of gain stages. For example, the analog section may comprise n stages, each of which applies a corresponding gain G_1, G_2, \dots, G_n (dB) respectively. Accordingly, the overall gain G is given by:

$$G = G_1 + G_2 + \dots + G_n.$$

[35] Referring to FIG. 4, each of the gain blocks i applies a gain G_i to a narrowband direct signal that it receives. In this regard, the gain block 406a applies an initial gain G_1 dB to the first narrowband signal NBDO-1 and generates an analog second narrowband signal NBDO-2. The gain block 406b applies a gain G_2 dB to the second narrowband signal NBDO-2 and generates an analog narrowband signal NBDO-3, and so on. The gain block 406(n) applies a gain G_n dB to the n^{th} narrowband signal NBDO- n and generates an output that is provided as an input to the ADC 408. The analog to digital converter (ADC) 408 converts the analog signal to digital samples, which are subsequently processed.

[36] In order to detect small receiver signal inputs, the overall initial gain G_{initial} may be set to a large value. However, if the incoming input signal X is itself large, then the overall signal $Y = (10^{(G_{\text{initial}}/20)}) * X$ seen at the output of the ADC 408 may be large enough such that clipping will occur at the ADC 408, and thus the output of the ADC 408 will not provide reliable information of the signal strength of X . In this case, it may not be possible to use the output of the ADC 408 to determine an appropriate final gain G_{final} to be applied to the data portion of the packet. To address this issue, if the outputs of the intermediate stages of the analog processing chain are available, for example WRSSI, NBDO-1, NBDO-2, then these outputs offer an earlier view of the signal before

it reaches the ADC. The signals, which are earlier in the processing chain, will necessarily be smaller and may not already be clipped. If the gains G_1, G_2, \dots, G_n are known, it is possible to narrow down a range within which the input signal lies.

[37] FIG. 5 is a block diagram illustrating variations in signal sizes as gain is applied to a baseband signal in the processing chain of FIG. 4 in accordance with an embodiment of the invention. Referring to FIG. 5, the receiver comprises a mixer 502, a low pass filter (LPF) block 504, a plurality of gain (G_i) blocks 506a, 506b, ... 506n, and analog-to-digital converter (ADC) block 508. As more gain is applied to the baseband signal, the signal gets larger and larger until clipping starts to occur.

[38] There may be, in general, insufficient granularity of the information provided by WRSSI, NBDO-1, NBDO-2, and so on, to determine a final packet gain G_{final} with enough accuracy with just the initial gain setting G_{initial} . Although the 802.11a/g OFDM preamble is relatively short in time compared to the length of the packet, there is still sufficient time to allow at least one intermediate gain change before a final gain has to be applied to the rest of the packet and still perform other tasks that need to be accomplished during the preamble. Thus, an intermediate gain $G_{\text{intermediate}}$ may be chosen based on the output of the analog gain stage outputs. If the intermediate gain is appropriately chosen, then digital samples will not clip, and these samples may be used to determine a good final gain value G_{final} .

[39] In general, the output of the intermediate gain stages requires processing in order to determine the received input signal power. The raw data by itself may be insufficient to accurately determine the power level. In an aspect of the invention, analog blocks may be utilized to accomplish this processing. While this may seem relatively simple to do, in practice, there may be enough variation to make the calculated received input power somewhat unreliable for an AGC algorithm. A more reliable method may comprise sampling the NBDO-x outputs with ADCs and calculating the power in the digital domain. In this regard, the variation in analog processing may be removed.

[40] FIG. 6 is a block diagram of a receiver 600 that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention. Referring to FIG. 6, the receiver comprises a mixer 602, a low pass filter (LPF) 604, a plurality of gain blocks (GCs) 606a, 606b, ..., 606n, analog-to-digital converters (ADC) 612a, 612b, 612c, ..., 612n, gain control block 614 and ADC processing paths 610a, 610b, ..., 610n. The receiver of FIG. 6 may also comprise a processor block 622 and a memory block 624.

[41] An input of the low pass filter 604 may be coupled to an output of the mixer 602. The plurality of gain blocks 606a, 606b, ..., 606n, may be serially coupled to an output of the low pass filter 604. A first ADC 612a of the plurality of analog-to-digital converters 612a, 612b, ..., 612n may be coupled to the output of the low pass filter 604. An input of each of a remaining portion of the analog-to-digital converters, namely 612b, ..., 612n, may be individually coupled to a corresponding output of each of the serially coupled gain blocks 606a, 606b, ..., 606n. In this regard, analog-to-digital converter 612b is coupled to an output of gain block 606a, and analog-to-digital converter 612n is coupled to an output of gain block 606(n-1) (not shown), for example. The analog-to-digital converter 608 may be coupled to an output of gain block 606n.

[42] An output path traced from the output of the low pass filter 604 to an output of each of the analog-to-digital converters 612a, 612b, ..., 612n may be referred to as a processing path. Accordingly, each of the processing paths may comprise a gain block and an analog to digital converter, except for the first processing path, which may have an analog-to-digital converter 612a coupled directly to the output of the low pass filter 604. Each of the processing paths may be adapted to apply a different gain to a narrow band direct out (NBDO) signal, which may be generated at an output of the low pass filter 604 and/or from successive serially coupled gain blocks 606a, 606b, ..., 606n. In the configuration of FIG. 6, processing path 610a may be utilized to ADC process signal NBDO-1, processing path 610b may be utilized to ADC process signal NBDO-2, ..., and processing path 610n may be utilized to ADC process signal NBDO-n.

[43] In an illustrative embodiment of the invention, consider a case where an 802.11 a/g OFDM preamble, for example, is received. If it is determined that a clip has occurred at the ADC during the preamble, the AGC algorithm may examine the WRSSI and NBDO-x values. The output(s) of these signals may be sampled, and a power calculation may be done based on a summation of the magnitude squared of the samples.

[44] Although it may not be feasible to have all NBDO-x outputs available as digital samples and since ADCs are relatively large power consuming devices, a subset of these signals may still be quite useful. For example, assume that only the NBDO-1 signal is being utilized and it is sampled for a short training period of 0.8 μ sec at a rate of 40 Msamples/sec. Then, the collected power is given by:

$$\text{Power from NBDO-1} = \sum_{j=1}^{32} (\text{samples of NBDO-1}(j))^2$$

[45] A value of the collected power may be compared to a table of thresholds, which correspond to an input signal power at fixed values. Based on this comparison, an intermediate gain may be applied which may enable detection of carrier sense (CS) and prevent clipping at the ADC. This intermediate gain may provide a more accurate power estimate that may be utilized by the automatic gain controller to determine a more precise final gain that is to be applied to the packet. In this regard a plurality of intermediate gains may be generated and these intermediate gains may be utilized to more accurately determine final gain to be applied to the packet by the AGC.

[46] FIG. 7 is a flow chart illustrating exemplary steps that may be utilized for improving dynamic range using upstream analog information in accordance with an embodiment of the invention. Referring to FIG. 7, in step 702, it may be determined whether a valid preamble is detected. If a valid preamble is determined, in step 710, the final gain G_{final} may be chosen and processing continued with step 702. If a valid preamble is not determined, in step 704, it may be determined whether the signal is clipped. If the signal is not clipped, processing continues at step 702. If the signal is

clipped, then over a specified period, for example, 0.8 microseconds (μs), NBDO samples may be collected and the power computed. In step 708, the computed power may be compared to a table of values and the appropriate intermediate gain $G_{\text{intermediate}}$ may be applied. The period or interval over which samples are collected may be implementation dependent and other values other than 0.8 μs may be utilized. The value 0.8 μs has particular significance in 802.11 a/g applications. Accordingly, other applications may utilize a different value.

[47] In instances where a wideband received signal (WRS) has not been bandwidth limited, a wideband received signal (WRSS) may not be digitally sampled without aliasing. However, analog processed information of the WRS may be band limited and may therefore be utilized to provide a useful measure for aiding with automatic gain control. In this regard, analog processed information of the WRS may be utilized to determine a more accurate final gain G_{final} and/or a more accurate distribution of that final gain, which may be derived from the individually applied gain values of G_1, G_2, \dots, G_n . In accordance with an embodiment of the invention, signals with a much wider dynamic range of received input powers may be more precisely demodulated. Accordingly, the receiver 600 of FIG. 6 provides a much wider dynamic range in the received input powers, which permits more accurate demodulation of the received signal.

[48] FIG. 8a is a diagram illustrating a case having no WRSSI and/or NBDO-x information, which may be utilized in connection with improving dynamic range using upstream analog information in accordance with an embodiment of the invention. Referring to FIG. 8a, there is illustrated a first range 802 and a second range 804. The range of 0 to -100 dBm is utilized for illustrative purposes and is not intended to limit the invention to a range of 100 dB. The first range 802 illustrates a range of received input powers where a fixed single value $G_{\text{intermediate}}$ results in a correctly received packet. The second range 804 illustrates a range of received input powers where a fixed single value G_{initial} results in a correctly received packet.

[49] Without access to information such as WRSSI and/or NBDO-x, higher received input powers cannot be differentiated using only the samples out of the ADC. If there is sufficient time to apply an intermediate gain $G_{\text{intermediate}}$, then at best only a single fixed value of $G_{\text{intermediate}}$ may be applied when a clip at the ADC has occurred. By doing so, the input range associated with this scheme may not be as large as would be otherwise possible if the intermediate gain value $G_{\text{intermediate}}$ was more intelligently chosen.

[50] FIG. 8b is a diagram illustrating a case having WRSSI and/or NBDO-x information, which may be utilized in connection with improving dynamic range using upstream analog information in accordance with an embodiment of the invention. Referring to FIG. 8b, there is illustrated a first range 818, a second range 816, a third range 814 up to an n^{th} range 812. The range of 0 to -100 dBm is utilized for illustrative purposes and is not intended to limit the invention to a range of 100 dB. The range 812 illustrates a range of received input powers where a fixed single value $G_{\text{intermediate},n}$ may be applied to result in a correctly received packet. The range 814 illustrates a range of received input powers where a fixed single value $G_{\text{intermediate},2}$ may be applied to result in a correctly received packet. The range 816 illustrates a range of received input powers where a fixed single value $G_{\text{intermediate},1}$ may be applied to result in a correctly received packet. The range 818 illustrates a range of received input powers where a fixed single value G_{initial} may be applied to result in a correctly received packet. Accordingly, with the use of WRSSI and/or NBDO-x information, the intermediate gain that is applied may take on one of a plurality of values, resulting in an extension of the dynamic range of received input powers that may be correctly demodulated for a received packet.

[51] With reference to FIG. 6, the receiver 600 may detect when a valid preamble for a packet is received and generate a plurality of upstream analog signals for the received packet. The generated plurality of upstream analog signals may comprise narrowband analog signals NBDO-1, NBDO-2, ..., NBDO-n. A processor 622 may acquire upstream analog information related to at least a portion of the generated plurality of upstream analog signals. At least one of a plurality of gain blocks 606a, 606b, ..., 606n may be

adapted to adjust a gain for the received packet using at least a portion of the acquired upstream analog information to increase a dynamic range of the received packet.

[52] The receiver 600 may further comprise at least one low pass filter 604 that is adapted to low pass filter the received packet. The processor 622 may acquire at least one sample from at least a portion the generated plurality of upstream analog signals NBDO-1, NBDO-2, ..., NBDO-n and compute a power based on the acquired sample. The processor 622 may be adapted to determine when at least one of the generated plurality of upstream analog signals NBDO-1, NBDO-2, ..., NBDO-n is clipped. At least one of the gain blocks 606a, 606b, ..., 606n may generate an intermediate gain based on the computed power of the acquired sample. The processor 622 may apply the generated intermediate gain to at least one of the generated plurality of upstream analog signals BDO-1, NBDO-2, ..., NBDO-n. After comparing the computed power to a plurality of defined power values, the processor 622 may select a gain based on a comparable power value. At least one of the gain blocks 606a, 606b, ..., 606n may be utilized to apply a final gain to the received packet upon detecting a valid preamble. The receiver 600 may be adapted to convert the generated plurality of upstream analog signals NBDO-1, NBDO-2, ..., NBDO-n to corresponding time domain signals.

[53] Aspects of the invention provide improvements in dynamic range for an AGC algorithm utilizing information further up the analog processing chain. Reliability is also improved by processing NBDO-x information in the digital domain. While this method was discussed with respect to an 802.11 OFDM system, the invention is not so limited and similar gains may be achieved for any receiver architecture that encounters similar types of issues.

[54] Accordingly, the present invention may be realized in hardware, software, or a combination of hardware and software. The present invention may be realized in a centralized fashion in at least one computer system, or in a distributed fashion where different elements are spread across several interconnected computer systems. Any kind of computer system or other apparatus adapted for carrying out the methods described herein is suited. A typical combination of hardware and software may be a

general-purpose computer system with a computer program that, when being loaded and executed, controls the computer system such that it carries out the methods described herein.

[55] The present invention may also be embedded in a computer program product, which comprises all the features enabling the implementation of the methods described herein, and which when loaded in a computer system is able to carry out these methods. Computer program in the present context means any expression, in any language, code or notation, of a set of instructions intended to cause a system having an information processing capability to perform a particular function either directly or after either or both of the following: a) conversion to another language, code or notation; b) reproduction in a different material form.

[56] While the present invention has been described with reference to certain embodiments, it will be understood by those skilled in the art that various changes may be made and equivalents may be substituted without departing from the scope of the present invention. In addition, many modifications may be made to adapt a particular situation or material to the teachings of the present invention without departing from its scope. Therefore, it is intended that the present invention not be limited to the particular embodiment disclosed, but that the present invention will include all embodiments falling within the scope of the appended claims.